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09/364,786	07/30/1999	Radhika Thekkath	0077.20	9876	
7590 02/11/2004 Sterne, Kessler, Goldstein & Fox PLLC			EXAM	EXAMINER	
			CHUNG, I	CHUNG, DANIEL J	
1100 New York Avenue NW Suite 600		ART UNIT	PAPER NUMBER		
Washington, DC 20005-3934			2672	17	
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Please find below and/or attached an Office communication concerning this application or proceeding.

1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) 4) Interview Summary (PTO-413) Paper No(s)/Mail Date 5) Notice of Informal Patent Application (PTO-152)	· ·	•					
## Examiner Daniel J Chung 2672 - The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Exercision of time may be available under the provisions of 37 CPR 1.196(a). In or event, however, may anyly be timely filled in the provision of 37 CPR 1.196(b). In or event, however, may anyly be timely filled in the provision of 37 CPR 1.196(b). In the period for relay is equilised above, the maintime statistically period will apply and will expire SIX (0) MONTHS from the mailing date of this communication in the provision is the timely specified above. The mainting date of this communication is the period of the communication. Any reply received by the Officia for than there more than the mainting date of this communication, work remained that the mainting date of this communication, work remained that the period of the communication is non-final. 3) Sequence of the period of the period of the communication is non-final. 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213. Disposition of Claims 4) Claim(s) 1-41 is/are pending in the application. 4a) Of the above claim(s) is/are allowed. 5) Claim(s) is/are allowed. 6) Claim(s) is/are allowed. 6) Claim(s) is/are allowed. 6) Claim(s) is/are allowed. 7) Claim(s) is/are allowed. 8) Craim(s) is/are allowed. 1) Acknowledgment drawing sheet(s) including the correction is required if the drawing(s) is objected to. Sea 37 CPR 1.121(d). 11) The each or declaration is objected to by the Examiner. 10) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). 2) All b) Acknowledgment is made of a claim for		Application No.	Applicant(s)				
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DETAILED ACTION

Claims 1-41 are presented for examination. This office action is in response to the Amendment filed on 10-22-2003.

Information Disclosure Statement

Receipt is acknowledged of Applicant's Information Disclosure Statement of 10-22-2003, which has been placed in the application file and considered by the Examiner.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-41 are rejected under 35 U.S.C. 103(a) as being unpatentable over Duluk, Jr. (5,977,987) in view of Heinrich ("MIPS R4000 Microprocessor User's Manual).

Regarding claim 1, Duluk, Jr. discloses that the claimed feature of a method for performing computer graphics calculations, method comprising: representing a vertex in a computer graphics image with a plurality of coordinates (See col 2 line 66-col 3 line 17, col 3 line 44-col 4 line 11, col 11 line 33-37); transforming plurality of coordinates

into a plurality of transformed coordinates (See col 1 line 35-58, col 2 line 66-col 3 line 17); using a floating point magnitude compare instruction to perform a magnitude comparison between at least a portion of plurality of transformed coordinates and a value representing a plurality of edges of a specified view volume, wherein comparison results for at least three view volume edges are obtained. (See Abstract, col 6 line 60-col 7 line 33, col 7 line 47-col 9 line 9, col 75 line 49-col 76 line 67)

Duluk, Jr. does not explicitly disclose that utilizing the set of compare instructions. However, such limitation is shown in the teaching of Heinrich. ["the floating-point compare (C.fmt.cond) instructions interpret the contents of two FPU registers (fs, ft) in the specified format (fmt) and arithmetically compare them"] (See p.171, Table 6-12, B-19) it would have been obvious to one skilled in the art to incorporate the teaching of Heinrich into the teaching of Duluk, Jr., in order to allow the processor for directly performing the specific calculations and operations during graphic rasterization, as such improvement [implementing "compare instructions"] is also advantageously desirable in the teaching of Duluk, Jr. for operating the rendering system with both hardware and software optimization.

Regarding claim 2, Duluk, Jr. discloses that plurality of transformed coordinates are processed in parallel. (See col 51 line 49-54, col 54 line 5-6)

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Regarding claim 3, refer to the discussion for the claim 1 hereinabove, Heinrich further discloses that setting a plurality of condition code bits to one or more specific states to indicate results of magnitude comparison. (See p.159, p.161, p.170)

Regarding claim 4, refer to the discussion for the claim 1 hereinabove, Heinrich further discloses that specifying a compare condition in floating point magnitude compare instruction. (See p.159, p.161, p.170)

Regarding claim 5, refer to the discussion for the claim 1 hereinabove, Heinrich further discloses that setting one of plurality of condition code bits to indicate true if an associated compare condition is true and setting one condition code bit to indicate false if associated compare condition is false. (See p.159, p.161, p.170)

Regarding claim 6, refer to the discussion for the claim 1 hereinabove, Heinrich further discloses that converting a plurality of fixed point values into a plurality of floating point values using a first convert instruction. (See p.170, B-10)

Regarding claim 7, refer to the discussion for the claim 1 hereinabove, Heinrich further discloses that first convert instruction is a CVT.PS.PW instruction. (See B-9, B-10)

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Regarding claim 8, refer to the discussion for the claim 1 hereinabove, Heinrich further discloses that converting a plurality of floating point values into a plurality of fixed point values using a second convert instruction. (See p.170, B-10, B-21, B-23)

Regarding claim 9, refer to the discussion for the claim 1 hereinabove, Heinrich further discloses that second convert instruction is a CVT.PS.PW instruction. (See B-9, B-10)

Regarding claim 10, refer to the discussion for the claim 1 hereinabove, Heinrich further discloses that floating point magnitude compare instruction is a CABS instruction. (See p.171, B-9, B-10, B-19)

Regarding claims 11-19, claims 11-19 are similar in scope to the claims 1 and 3-10, and thus the rejections to claims 1 and 3-10 hereinabove are also applicable to claims 11-19.

Regarding claims 20-21 and 24-25, claims 20-21 and 24-25 are similar in scope to the claims of 1-3, and thus the rejections to claims of 1-3 hereinabove are also applicable to claims 20-21 and 24-25.

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Regarding claim 22, refer to the discussion for the claim 1 hereinabove, Heinrich further discloses that first instruction is part of a general purpose instruction set architecture. (See p.159, p.161, p.170, p.171)

Regarding claim 23, refer to the discussion for the claim 1 hereinabove, Heinrich further discloses that first instruction is part of an application specific extension to a general purpose instruction set architecture. (See p.159, p.161, p.170, p.171)

Regarding claim 26, refer to the discussion for the claim 1 hereinabove, Heinrich further discloses that first instruction is executed in a single clock cycle. (See p.159, p.161, p.170, p.171)

Regarding claims 27-28, claims 27-28 are similar in scope to the claim 1, and thus the rejection to claim 1 hereinabove is also applicable to claims 27-28.

Regarding claim 29, refer to the discussion for the claim 1 hereinabove, Heinrich further discloses that plurality of operands are in a paired-single data format. (See p.159, p.161, p.170, p.171)

Regarding claims 30-37, claims 30-37 are similar in scope to the claims of 2 and 20-23 and thus the rejections to claims of 2 and 20-23 hereinabove are also applicable to claims 30-37.

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Regarding claims 38-41, claims 38-41 are similar in scope to the claims of 1,6 and 29 and thus the rejections to claims of 1,6 and 29 hereinabove are also applicable to claims 38-41.

Response to Arguments/Amendments

Applicant's arguments with respect to claims 1-41 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daniel J. Chung whose telephone number is (703) 306-3419. He can normally be reached Monday-Thursday and alternate Fridays from 7:30am- 5:00pm. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael, Razavi, can be reached at (703) 305-4713.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

or faxed to:

(703) 872-9306 (Central fax)

(703) 872-9314 (for Technology Center 2600 only)

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.

djc

February 6, 2004

MÍCHAEL RAZAVI

SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2600